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PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

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Application Number	10766,131
Filing Date	January 28, 2004
First Named Inventor	Perego
Art Unit	2188
Examiner Name	
Attorney Docket Number	RAMB-01049US2

ENCLOSURES (Check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Kirk J. DeNiro, Esq., Vierra Magen Marcus Harmon & DeNiro LLP	
Signature		
Printed name	Kirk J. DeNiro	
Date	January 24, 2005	Reg. No. 35,854

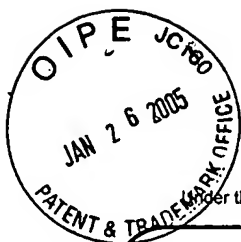
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/766,131
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First Named Inventor	Perego
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Attorney Docket Number	RAMB-01049US2

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
		SCI, "P1596: SCI, A Scalable Coherent Interface", November 28, 1988	
		GUSTAVSON et al., DRAFT "The Scalable Coherent Interface Project (SuperBus)", SCI, August 22, 1988, Rev. 14	
		SCHANKE, "Proposal for Clock Distribution in SCI", SCI, May 5, 1989	
		JAMES, "Scalable I/O Architecture for Buses", COMPCON Spring 1989, SCI, November 28, 1988	
		VOLZ et al., "Position Paper On Global Clock For the Futurebus +", SCI, 1989	
		KRISTIANSEN et al., "Scalable Coherent Interface", to appear in Eurobus Conference Proceedings, May 1989	

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